

TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
TURNS RATIO'S: PRI : SEC #1 : SEC #2	-----	1:2:2	-----	± 3%
PRIMARY INDUCTANCE: (9 to 7) VOLTAGE = 0.200Vrms @ 10KHz	1600	2350	3000	μHy
PRIMARY LEAKAGE IND: VOLTAGE = 0.200Vrms @ 10KHz	-----	0.90	2.00	μHy
UNIPOLEAR DESIGN POINT: 15V Pri OPERATING FREQUENCY MAXIMUM DUTY CYCLE: UNIPOLEAR MAXIMUM FLUX DENSITY: BIPOLAR MAXIMUM FLUX DENSITY: UNIPOLEAR ET PRODUCT WITH <10% DROOP: ⁽¹⁾ (PRIMARY VOLTS x PULSE WIDTH) BIPOLAR PULSE @ Bmax_Bipolar UNIPOLEAR PULSE @ Bmax = Unipolar	-----	38	80	KHz
	-----	-----	50	%
	-----	-----	.13	Tesla
	-----	-----	.26	Tesla
	-----	90	100	V-μsec
	-----	190	200	V-μsec
HI-POT: BETWEEN ANY TWO WINDINGS	2500	-----	-----	Vrms

(1) Higher ET's are possible at lower operating frequencies and/or if increased pulse droop can be tolerated. High frequency designs are generally core loss limited.

FIGURE 1: SCHEMATIC DIAGRAM

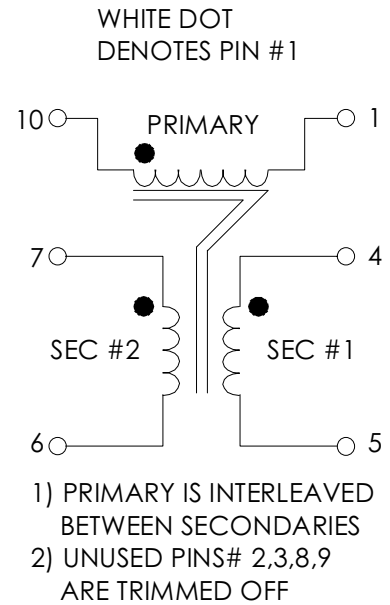


FIGURE 2: PHYSICAL DIMENSIONS IN INCHES, (mm)

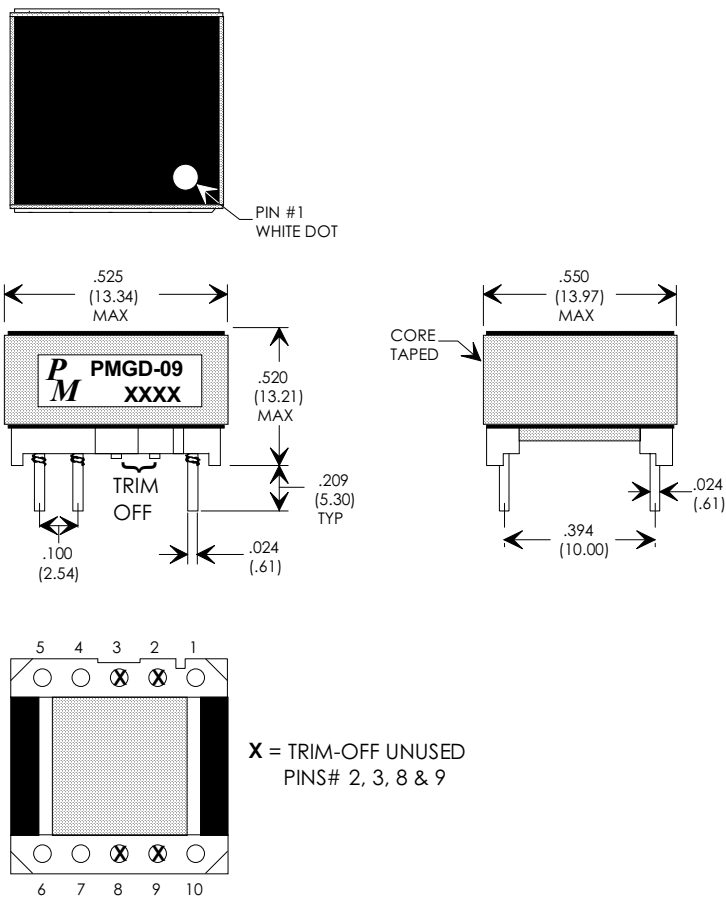
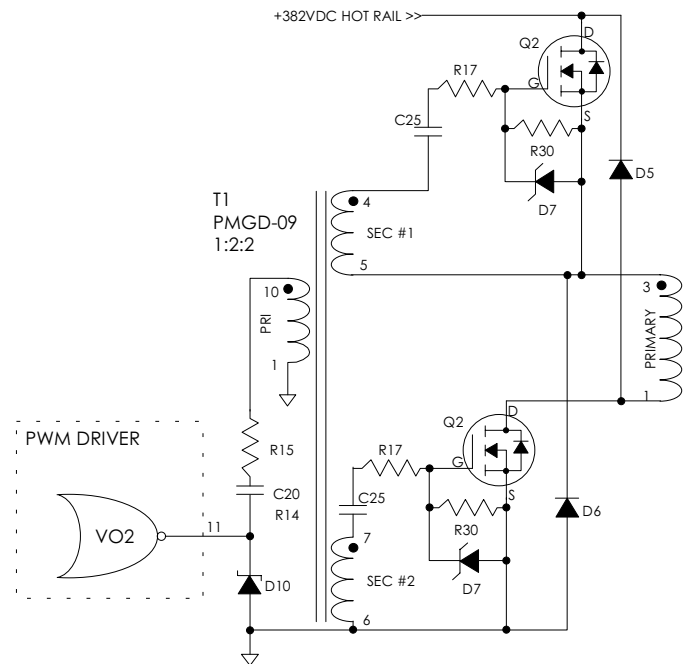


FIGURE 3: TYPICAL UNIPOLEAR DRIVE CIRCUIT



EP13, 10-PIN

REV.	DESCRIPTION OF CHANGES
08/08/96	INITIAL RELEASE
09/03/96	UPDATED TO TRIM-OFF UNUSED PINS# 2,3,8 & 9

GATE DRIVE TRANSFORMER CONTROL DRAWING

PREMIER P/N: PMGD-09	REVISION: 9/03/96
DRAWN BY: TOM O'NEIL	REF:
SCALE: NONE	SHEET: 1 OF 2